

**CLAIMS:**

2           1.       A method comprising:  
starting a timer defined for use within a first wireless communication system; and  
4           estimating a duration of a transition from the first wireless communication system  
to a second wireless communication system as a function of the timer.

2           2.       The method of claim 1, further comprising performing a pre-defined  
operation associated with the timer.

2           3.       The method of claim 2, wherein the operation is pre-defined by the first  
wireless communication system.

4           4.       The method of claim 1, wherein the timer comprises a supervision timer.

2           5.       The method of claim 1, wherein the timer is defined by the IS856 wireless  
communication standard.

2           6.       The method of claim 1, further comprising:  
starting a plurality of timers defined for use within the first wireless  
communication system; and  
4           when returning to the first wireless communication system, estimating the  
duration of the transition as a function of the plurality of timers.

2           7.       The method of claim 1, wherein the first wireless communication system  
is an IS856 system and the second wireless communication system is an IS2000-1x  
system.

2           8.       The method of claim 7, wherein the supervision timer comprises an IS856  
Control Channel Supervision Timer.

9. The method of claim 8, further comprising:

attempting to receive a synchronous control channel capsule; and  
transitioning to a network acquisition state when the attempt to receive the  
synchronous control channel capsule is unsuccessful.

10. The method of claim 7, wherein the supervision timer comprises a data  
rate control (DRC) supervision timer, the method further comprising:  
starting a combination timer; and  
when returning to the IS856 system, estimating the duration of the transition as a  
function of the DRC supervision timer and the combination timer.

11. The method of claim 10, further comprising:  
restarting a transmitter in response to expiration of the DRC supervision timer;  
and  
transitioning to an inactive state in response to expiration of the combination  
timer.

12. A processor-readable medium containing processor executable  
instructions for:  
starting a timer defined for use within a first wireless communication system; and  
estimating a duration of a transition from the first wireless communication system  
to a second wireless communication system as a function of the timer.

13. The processor-readable medium of claim 12, containing further  
instructions for performing a pre-defined operation associated with the timer.

14. The processor-readable medium of claim 13, wherein the operation is pre-  
defined by the first wireless communication system.

15. The processor-readable medium of claim 12, wherein the timer comprises  
a supervision timer.

16. The processor-readable medium of claim 12, wherein the timer is defined  
2 by the IS856 wireless communication standard.

17. The processor-readable medium of claim 12, containing further  
2 instructions for:  
starting a plurality of timers defined for use within the first wireless  
4 communication system; and  
when returning to the first wireless communication system, estimating the  
6 duration of the transition as a function of the plurality of timers.

18. The processor-readable medium of claim 12, wherein the first wireless  
2 communication system is an IS856 system and the second wireless communication  
system is an IS2000-1x system.

19. The processor-readable medium of claim 18, wherein the supervision  
2 timer comprises an IS856 Control Channel Supervision Timer.

20. The processor-readable medium of claim 19, containing further  
2 instructions for:  
attempting to receive a synchronous control channel capsule; and  
4 transitioning to a network acquisition state when the attempt to receive the  
synchronous control channel capsule is unsuccessful.

21. The processor-readable medium of claim 18, wherein the supervision  
2 timer comprises a data rate control (DRC) supervision timer, the processor-readable  
medium containing further instructions for:  
4 starting a combination timer; and  
when returning to the IS856 system, estimating the duration of the transition as a  
6 function of the DRC supervision timer and the combination timer.

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22. The processor-readable medium of claim 21, containing further  
2 instructions for:  
restarting a transmitter in response to expiration of the DRC supervision timer;  
4 and  
transitioning to an inactive state in response to expiration of the combination  
6 timer.

23. A wireless communication device comprising:  
2 first wireless communication system hardware for operating in a first wireless  
communication system;  
4 second wireless communication system hardware for operating in a second  
wireless communication system;  
6 an interoperation module to configure the wireless communication device in  
response to a transition between the first and second wireless communication systems,  
8 the interoperation module configured to estimate a duration of the transition as a function  
of a supervision timer.

24. The wireless communication device of claim 23, wherein the  
2 interoperation module is configured to estimate the duration of the transition as a function  
of a plurality of supervision timers.

25. The wireless communication device of claim 23, wherein the first wireless  
2 communication system is an IS856 system and the second wireless communication  
system is an IS2000-1x system.

26. The wireless communication device of claim 25, wherein the supervision  
2 timer is a Control Channel Supervision Timer.

27. The wireless communication device of claim 26, wherein the  
2 interoperation module is configured to:  
attempt to receive a synchronous control channel capsule; and

4 transition to a network acquisition state when the attempt to receive the  
synchronous control channel capsule is unsuccessful.

28. The wireless communication device of claim 25, wherein the supervision  
2 timer is a data rate control (DRC) supervision timer, and wherein the interoperation  
module is configured to:

4 start a combination timer; and  
when returning to the IS856 system, estimate the duration of the transition as a  
6 function of the DRC supervision timer and the combination timer.

29. The wireless communication device of claim 28, wherein the  
2 interoperation module is configured to:

restart a transmitter in response to expiration of the DRC supervision timer; and  
4 transition to an inactive state in response to expiration of the combination timer.

30. An apparatus comprising:  
2 means for starting a timer defined for use within a first wireless communication  
system; and  
4 estimating a duration of a transition from the first wireless communication system  
to a second wireless communication system as a function of the timer.

31. The apparatus of claim 30, further comprising means for performing a pre-  
2 defined operation associated with the timer.

32. The apparatus of claim 31, wherein the operation is pre-defined by the  
2 first wireless communication system.

33. The apparatus of claim 30, wherein the timer comprises a supervision  
2 timer.

34. The apparatus of claim 30, wherein the timer is defined by the IS856  
2 wireless communication standard.

35. The apparatus of claim 34, further comprising:  
2 means for starting a plurality of timers defined for use within the first wireless  
communication system; and  
4 means for estimating the duration of the transition as a function of the plurality of  
timers when returning to the first wireless communication system.

36. The apparatus of claim 30, wherein the first wireless communication  
2 system is an IS856 system and the second wireless communication system is an IS2000-  
1x system.

37. The apparatus of claim 36, wherein the supervision timer comprises an  
2 IS856 Control Channel Supervision Timer.

38. The apparatus of claim 37, further comprising:  
2 means for attempting to receive a synchronous control channel capsule; and  
means for transitioning to a network acquisition state when the attempt to receive  
4 the synchronous control channel capsule is unsuccessful.

39. The apparatus of claim 36, wherein the supervision timer comprises a data  
2 rate control (DRC) supervision timer, the apparatus further comprising:  
means for starting a combination timer; and  
4 means for estimating the duration of the transition as a function of the DRC  
supervision timer and the combination timer when returning to the IS856 system.

40. The apparatus of claim 39, further comprising:  
2 means for restarting a transmitter in response to expiration of the DRC  
supervision timer; and

4 means for transitioning to an inactive state in response to expiration of the  
combination timer.

41. A system comprising:  
2 a memory that stores processor-readable instructions; and  
a processor coupled to the memory that executes the instructions to start a timer  
4 defined for use within a first wireless communication system and to estimate a duration  
of a transition from the first wireless communication system to a second wireless  
6 communication system as a function of the timer.

42. The system of claim 41, wherein the processor further executes the  
2 instructions to perform a pre-defined operation associated with the timer.

43. The system of claim 42, wherein the operation is pre-defined by the first  
2 wireless communication system.

44. The system of claim 41, wherein the timer comprises a supervision timer.

45. The system of claim 41, wherein the timer is defined by the IS856 wireless  
2 communication standard.

46. The system of claim 41, wherein the processor further executes the  
2 instructions to:  
start a plurality of timers defined for use within the first wireless communication  
4 system; and  
when returning to the first wireless communication system, estimate the duration  
6 of the transition as a function of the plurality of timers.

47. The system of claim 41, wherein the first wireless communication system  
2 is an IS856 system and the second wireless communication system is an IS2000-1x  
system.

48. The system of claim 47, wherein the supervision timer comprises an IS856  
2 Control Channel Supervision Timer.

49. The system of claim 48, wherein the processor further executes the  
2 instructions to:  
attempt to receive a synchronous control channel capsule; and  
4 transition to a network acquisition state when the attempt to receive the  
synchronous control channel capsule is unsuccessful.

50. The system of claim 47, wherein the supervision timer comprises a data  
2 rate control (DRC) supervision timer, and wherein the processor further executes the  
instructions to:  
4 start a combination timer; and  
when returning to the IS856 system, estimate the duration of the transition as a  
6 function of the DRC supervision timer and the combination timer.

51. The system of claim 50, wherein the processor further executes the  
2 instructions to:  
restart a transmitter in response to expiration of the DRC supervision timer; and  
4 transition to an inactive state in response to expiration of the combination timer.